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Advanced Process & Module Development (DRAM / Flash Memory / Logic, etc.)

- Photolithography (e-beam, EUV, OPC/RET, Photo Resist Materials)

Dry Etch, Cleaning & CMP, Diffusion, Ion Implantation, Metallization, Metrology & Inspection, etc.

- Device Isolation, Transistor, Capacitor, Dielectric

- High-K/Metal Gate, SiO2/SiON Gate Dielectric

- Low-K, Interconnect, etc.

FEOL/BEOL Process Integration (DRAM / Flash / Logic, etc.)

New Memory : PRAM, STT-MRAM, ReRAM, etc.

TCAD/ECAD

- Process & Device Modeling, Material Modeling

- Circuit Compact/Reliability Modeling

- Circuit Simulator Development

Advanced CMOS Image Sensor (CIS) Development

- Pixel Design, Image Signal Processing, Mixed IC Design, Analog/Digital Design

- CIS Process Development

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