PRESS RELEASE

Gwangju Institute of Science and Technology Researchers Reveal the Effect of AIN Surface Pits on GaN Remote Epitaxy

The pits damage the graphene layer between AIN template and GaN thin film during growth at high temperatures, causing the failure of film exfoliation

Remote epitaxy, a promising technology for thin film growth and exfoliation, suffers from substrate damage under harsh conditions. In this regard, researchers recently investigated the effect of nano-sized pits on AIN template surface on GaN remote epitaxy. While GaN thin film could be exfoliated at 750 °C, it failed at 1050 °C. At higher temperatures, the nano pits damaged the graphene layer between the template and the film, causing alterations in the film growth mode.



Image Title: Effect of AIN surface pits on GaN remote epitaxy

Image Caption: The presence of nano-sized pits on the AIN template surface damages the graphene layer above it at higher temperatures. This changes the growth mode of GaN thin film, making its exfoliation unsuccessful. Image Credit: Dong-Seon Lee from Gwangju Institute of Science and Technology

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Remote epitaxy has been gaining attention in the field of semiconductor manufacturing for growing thin films that copy the crystal structure of the template, which can later be exfoliated to form freestanding membranes. However, harsh epitaxy conditions can often cause damage to the template materials, such as in the case of remote epitaxy of GaN thin films, promising materials for light-emitting diodes, photodetectors, and power electronic devices, on graphene/AIN templates.

GaN remote heteroepitaxy has not been achieved by a standard metal–organic chemical vapor deposition (MOCVD) technique due to the high temperatures involved in the process.

It has been reported that graphene placed on a substrate in an extreme environment such as high temperature or use of an active gas in MOCVD gets damaged due to chemical instability, which causes failure to exfoliate grown GaN films.

Against this background, a team of researchers led by Dong-Seon Lee, Head of the Department of Semiconductor Engineering and Professor at the School of Electrical Engineering and Computer Science at the Gwangju Institute of Science and Technology, has recently used remote epitaxy to grow GaN thin films on graphene/AIN templates by MOCVD and investigated the effect of surface pits in AIN on the growth and exfoliation of these thin films. Their paper was made available online on 6 June 2023 and published in Volume 17, Issue 12 of the journal <u>ACS Nano</u> on 27 June 2023.

The researchers first performed an annealing test at 950 °C for 5 minutes to check the thermal stability of graphene on AlN. Based on its results, they developed a two-step process to grow GaN thin films on the template by MOCVD. The first GaN growth took place at 750 °C for 10 minutes, following which the second growth was performed at 1050 °C for 60 minutes. The exfoliation of the thus-grown GaN thin films was used as a proof of success of the remote epitaxy process. While the films grown at 750 °C could be exfoliated successfully, the separation failed after the second step growth.

Upon deeper analysis, the team found that the nano-sized pits on AIN surface led to the degradation of graphene near them at higher temperatures, which altered the growth modes of GaN thin films. As a result, GaN directly bonded with the AIN substrate, causing the failure of film exfoliation. *"Through this study, we revealed for the first time that structural problems in the substrate can also cause peeling failure. These results exemplify the importance of chemical and topographic properties of templates for successful remote epitaxy,"* highlights Prof. Lee.

This study provides the primary experimental data that supports the stable implementation of the development of remote epitaxy. When asked about the implications of the present work, Prof. Lee says: "In the near future, GaN remote epitaxy implementation is expected to provide high-quality GaN semiconductors required for the electric vehicle industry. Since substrate recycling is possible, it is expected to change the big picture of the existing semiconductor industry. Further, it will be possible to overcome Moore's law."

Let us hope that this technology contributes to the development of science and technology by enabling the production of various types of novel semiconductors!

Reference

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Prof. Dong-Seon Lee is Dean of the Department of Semiconductor Engineering and Professor of Electrical Engineering and Computer Science at the Gwangju Institute of Science and Technology (GIST), Korea. He received a Ph.D. in Electrical and Computer Engineering/Computer Science from the University of Cincinnati in 2002. Before joining GIST, he served as a researcher at Samsung Electronics and Seoul Optodevice Co. Ltd., Korea for eight years. He was President of the Korea Society of LED and Optoelectronics and an active committee member of international conferences such as IWN and ICNS. His research group focuses on GaN growth through various methods and fabrication of nano- and micro-LEDs for display applications.